

### **REMARKS/ARGUMENTS**

By this amendment, claims 1, 9 and 11 are amended. Support can be found in Paragraph [0025] of the specification. No new matter is introduced.

Claim Rejections – 35 USC § 103. The Office Action rejected claims 1-17 under Section 103(a) as being unpatentable over Datar et al. (U.S. 6,625,740) in view of Ellenby et al. (U.S. 6,064,398) or Dendinger (U.S. 6,714,891). The Office Action rejected claims 1-17 under Section 103(a) as being unpatentable over Miller (US 2002/0144030) in view of Ellenby et al. or Dendinger). Thus, the Datar et al, Ellenby et al, Dendinger and Miller patents have been cited to reject the pending patent claims. The Applicant respectfully traverses the rejections for the following reasons.

#### **Re claims 1-8**

None of the cited references disclose or suggest that the image data is directly written into the AGP memory block of the system memory instead of the non-AGP memory block of the system memory, as recited in the amended claim 1. According to the Office Action, Datar teaches an AGP bus which is used for the graphics accelerator to directly access a portion of the system memory, such as, AGP memory block. However, even if the AGP memory block can be directly accessed by the graphics accelerator, it is still possible that the image data is not directly written into the AGP memory block. For example, while the prior art allows the graphics accelerator to directly access AGP memory block of the system memory, the image data is written into a non-AGP memory block of the system memory before being transferred to the AGP memory block. In Datar there is no evidence showing that the image data stored in the AGP memory block to be accessed by the graphics accelerator was previously directly written into the AGP memory block instead of the non-AGP memory block.

The Office Action further indicates that Miller teaches an AGP bus (probably AGP slot) which is used for the graphics accelerator to directly access a portion of the system memory, such as, AGP memory block. Likewise, in Miller there is no evidence showing that the image data stored in the AGP memory block to be accessed by the graphics accelerator was previously directly written into the AGP memory block instead of the non-AGP memory block. Given that

the cited prior art patents do not positively teach the step of writing the image data directly into the AGP memory block of the system memory instead of the non-AGP memory block, Applicants respectfully submit that claims 1-8 would not have been obvious from the combined teachings of Datar et al, Ellenby et al, Dendinger and Miller, as set forth in the Office Action. Applicants respectfully request withdrawal of the rejections.

**Re claims 9-17**

None of the cited references disclose or suggest that the received data is written into a specified memory block of said system memory, which is accessible by said graphics accelerator, without transference of the image data inside the system memory, as recited in the amended claim 9 or 11. According to the Office Action, Datar teaches an AGP bus which is used for the graphics accelerator to directly access a portion of the system memory, such as, AGP memory block. However, even if the AGP memory block can be directly accessed by the graphics accelerator, it is still possible that transference of the image data into the AGP memory block from another memory block inside the system memory is performed. For example, while the prior art allows the graphics accelerator to directly access AGP memory block of the system memory, the image data is written into a non-AGP memory block of the system memory before being transferred to the AGP memory block. In Datar, there is no evidence showing that the image data stored in the AGP memory block to be accessed by the graphics accelerator was written into the AGP memory block without transference inside the system memory.

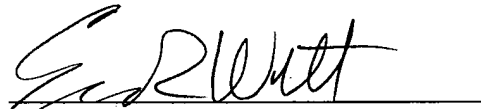
The Office Action further indicates that Miller teaches an AGP bus (probably AGP slot) which is used for the graphics accelerator to directly access a portion of the system memory, such as, AGP memory block. Likewise, in Miller there is no evidence showing that the image data stored in the AGP memory block to be accessed by the graphics accelerator was written into the AGP memory block without transference inside the system memory. Given that the cited prior art patents do not positively teach the step of writing the image data into a specified memory block of the system memory, which is accessible by the graphics accelerator, without transference of the image data inside the system memory, Applicants respectfully submit that claims 9-17 would not have been obvious from the combined teachings of Datar et al, Ellenby et

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al, Dendinger and Miller, as set forth in the Office Action. Applicants respectfully request withdrawal of the rejections.

Based on the above description, allowance of all pending claims 1-17 is respectfully requested. If there are any remaining issues preventing allowance of the pending claims that may be clarified by telephone, the Examiner is requested to call the undersigned.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Evan R. Witt", is written over a horizontal line.

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